

Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims:

Listing of Claims:

1-2. (Canceled)

3. (Currently amended) The An image data processing apparatus as defined in Claim 1 for performing a predetermined signal processing on input image data and generating output data for supplying to an external apparatus, the image data processing apparatus comprising:

a first processing circuit for performing a first signal processing with respect to image data so as to produce a first set of image data; and

a second processing circuit for performing with respect to the image data a second signal processing, which is independent from the first signal processing, so as to produce a second set of image data, wherein

the first processing circuit comprises a first filter for attenuating a first bandwidth of the image data so as to generate a first luminance data, a gamma corrector for performing a gamma correction processing on the first luminance data, and a first contour emphasizez for performing a first contour emphasis processing with respect to the gamma-corrected data; ~~and~~

the second processing circuit comprises a second filter for attenuating a second bandwidth of the image data so as to generate a second luminance data, and a second contour emphasizez for performing a second contour emphasis processing with respect to the second luminance data; ~~and~~

the first and second processing circuits execute the first and second signal processing in a parallel manner, and output the first and second sets of image data as output data.

4. (Original) The image data processing apparatus as defined in Claim 3, wherein

the first processing circuit further comprises a conversion circuit for changing at least one of pixel density or frame rate of the luminance data.

5. (Previously presented) The image data processing apparatus as defined in Claim 4, wherein

the second processing circuit further comprises a digitizer for digitizing an output from the second contour emphasize so as to generate a digitized data.

6. (Previously presented) The image data processing apparatus as defined in Claim 5, further comprising

a synthesizing circuit for allocating an output from the first processing circuit and an output from the second processing circuit to predetermined high-order bits and predetermined low-order bits of the output data, respectively, so as to synthesize the outputs.